



承认书

| 产品型号: DW1560B7Y1 | (Color gamut: 69%) |
|------------------|----------------------|
|------------------|----------------------|

产品规格: 1920RGB * 1080 INNO TFT LCM

| 制订 | 审 查 | 核准 | 公司印章 |
|------------|-----|------------|------|
| 蒋西吉 | | 2 tout | |
| 2022,08,05 | | 2022,08,05 | |

承 认 方:

| 研发部 | 工程部 | 品质部 | 核准 | 公司印章 |
|-----|-----|-----|----|------|
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- 临时规格书
- ☑ 正式规格书





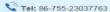




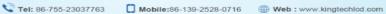
Document Revision History

| Version | Date | Page | Description | Changed By |
|---------|------------|------|-------------|------------|
| V00 | 2022-08-05 | - | First issue | 王加林 |
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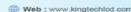






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1. LCM Specification

1.1 Description

DW1560B7Y1 is a transmissive type color active matrix liquid crystal display(LCD) which uses amorphous thin film transistor(TFT) as switching devices. This product is composed of a TFT LCD panel, 4 drives IC, a PCBA and a LED-backlight unit. The active display area is 15.6 inches diagonally measured and the native resolution is 1920*RGB*1080. Features of this product are listed in the following table.

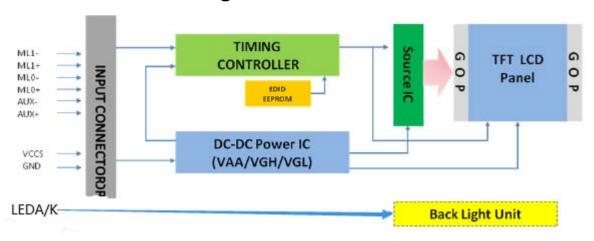
1.2 Functions & Features

Table 1.1 Module Functions & Features

| Table 1.1 Module I diletions & I catules | | | | | | |
|--|---|-------------------|--|--|--|--|
| Parameter | Value | Unit | | | | |
| LCD Mode | TFT/Transmissive | - | | | | |
| Color Depth | 16.7M | - | | | | |
| Display Resolution | 1920RGB*1080 | pixels | | | | |
| Module Size | 211.7(H)*359.4(W)*5.50(T)(Exclude PCBA) | mm | | | | |
| Active Area (A.A) | 193.59(H)*344.16(W) | mm | | | | |
| Pixel Arrangement | RGB-stripe | - | | | | |
| Viewing Direction | ALL | | | | | |
| Display Mode | Normally Black | | | | | |
| LCD Controller/Driver | TBD | - | | | | |
| IC Package Type | COG | - | | | | |
| Interface | eDP | - | | | | |
| Power Supply Voltage | 3.0~3.6 (Cell 0.9W max) | V | | | | |
| LCM Brightness | 1200 (Type.) | cd/m ² | | | | |
| Back-light | White LED*100 | PCS | | | | |

Note: The specified power consumption (with converter efficiency) is under the conditions at VCCS = 3.3 V, fv = 60 Hz, and Ta = 25 \pm 2 °, whereas **Mosaic** pattern is displayed.

1.3 Function Block Diagram





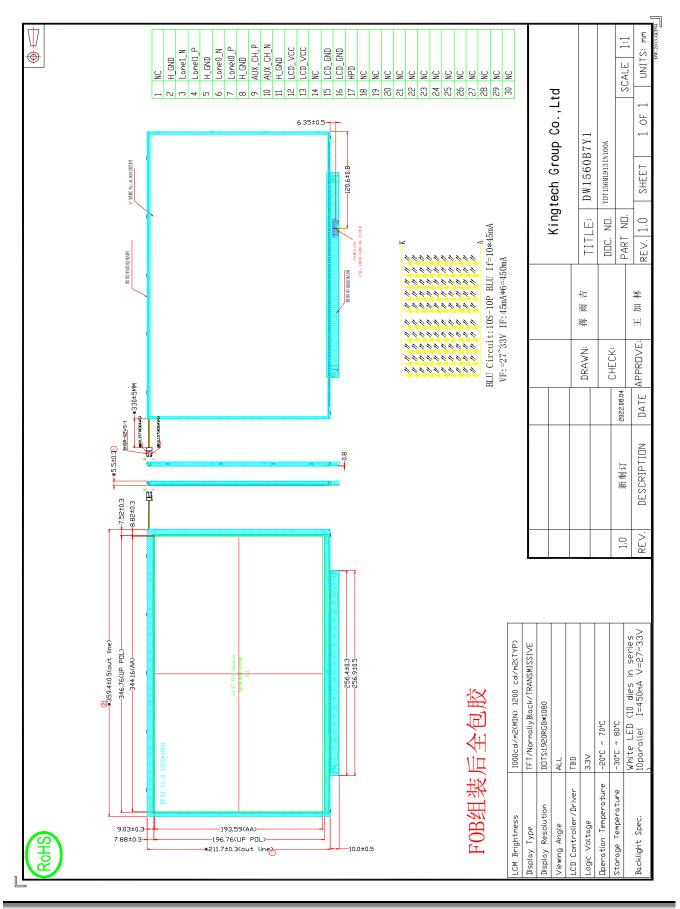




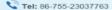




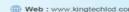
2. Mechanical Specification













3. Pin Descriptions

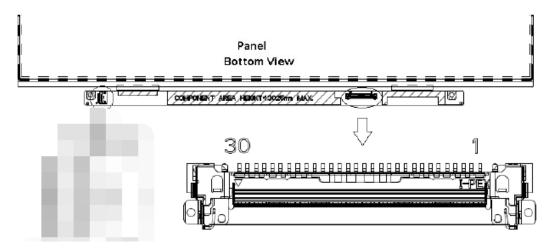
3.1 Input CONN Pin Assignment

Input Connector (I-pex 20455-030E-76) is used for the module electronics interface, the recommended CONN of user is 20453-030T-03 manufactured by I-pex.

| Pin No. | Symbol | I/O | Functional | Remark |
|---------|-----------|-----|---------------------------------------|--------|
| 1 | NC | I | No Connection.(Reserved for LCD test) | |
| 2 | H_GND | Р | System ground. | |
| 3 | ML1- | I/O | eDP RX channel 1 negative | |
| 4 | ML1+ | I/O | eDP RX channel 1 positive | |
| 5 | H_GND | Р | System ground. | |
| 6 | ML0- | I/O | eDP RX channel 0 negative | |
| 7 | ML0+ | I/O | eDP RX channel 0 positive | |
| 8 | H_GND | Р | System ground. | |
| 9 | AUX+ | I/O | eDP AUX CH positive | |
| 10 | AUX- | I/O | eDP AUX CH negative | |
| 11 | H_GND | Р | System ground. | |
| 12-13 | VCCS_3.3V | Р | Power Supply, 3.3V (typ.) | |
| 14 | NC | I | No Connection. | |
| 15-16 | H_GND | Р | System ground. | |
| 17 | HPD | 0 | Hot plug detect output. | |
| 18-21 | N.C | Р | No Connection. | |
| 22 | N.C | I | No Connection. | |
| 23 | N.C | 0 | No Connection. | |
| 24-25 | N.C | Р | No Connection. | |
| 26-29 | N.C | Р | No Connection. | |
| 30 | N.C | I | No Connection. | |

3.2 CONN PIN1 Location

Input CONNis on bottom side of PCBA, pin 1 location shows as following figure.











4. Electrical Units

4.1 Absolute Maximum Ratings

The absolute maximum ratings are list on Table 4.1. When used out of the absolute maximum ratings, the LCM may be permanently damaged. Using the LCM within the following electrical characteristics limit is strongly recommended for normal operation. If these electrical characteristic conditions are exceeded during normal operation, the LCM will malfunction and cause poor reliability.

Table 4.1 Module Absolute Maximum Ratings

| Item | Symbol | Unit | Value | Note |
|--------------------------|-----------|-------|--------------|------|
| Power Supply Voltage (1) | Vdd | V | -0.3 to +4.0 | |
| Power Supply Voltage (2) | VGH ~ VSS | V | * | |
| Power Supply Voltage (3) | VSS ~ VGL | V | * | |
| Operating Temperature | Тор | °C | -20 to +70 | |
| Storage Temperature | Tst | °C | -30 to +80 | |
| Operating Humidity | Нор | %(RH) | 10~85 | |

(VSS=0V)

4.2 LCD Electrical characteristics (Ta=25°C)

| Parameter | | Cumbal | | Value | Unit | Note | |
|-------------------------|-----------------|-------------------|------|-------|------|-------|---------|
| | | Symbol | Min. | Тур. | Max. | Offic | Note |
| Power Supply Voltage | | vccs | 3.0 | 3.3 | 3.6 | V | (1) |
| Ripple Voltage | | V_{RP} | - | 50 | 150 | mV | (1) |
| Inrush Current | | I _{RUSH} | 0 | - | 1.5 | Α | (1),(2) |
| Power Supply Current | Mosaic | | 210 | 240 | 270 | mΑ | (3) |
| | Black | l _{vccs} | 190 | 220 | 250 | mΑ | (3) |
| | H 1 line Stripe | | 450 | 550 | 650 | mΑ | |
| HPD Pull-Low Resistance | | R _{HPD} | 30K | - | 100K | ohm | (4) |
| HPD | High Level | VH _{HPD} | 2.25 | - | 2.75 | ٧ | (5) |
| luen. | Low Level | VL _{HPD} | 0 | - | 0.4 | ٧ | (5) |

Note (1) The ambient temperature is Ta = 25 \pm 2 °.

Note (2) IRUSH: the maximum current when VCCS is rising

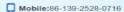
lis: the maximum current of the first 100ms after power-on

Measurement Conditions: Shown as the following figure. Test pattern: black.

DW1560B7Y1 6 OF 23

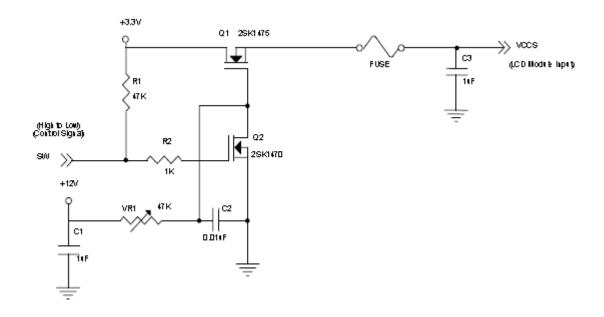




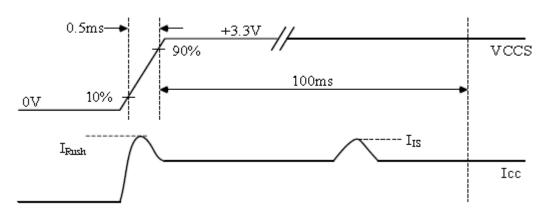




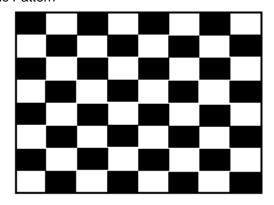




VCCS rising time is 0.5ms



Note (3) The specified power supply current is under the conditions at VCCS = 3.3 V, Ta = 25 \pm 2 $^{\circ}$, DC Current and f_v = 60 Hz, whereas a specified power dissipation check mosaic pattern is displayed Mosaic Pattern



Active Area

Note (4) The specified signals have pull down resistor to ground in the LCD module respectively. Customers should keep the input signal level requirement with the load of LCD module. Please refer to Note (4)







of 4.3 Back-light spec to obtain more information.

Note (5) When a source detects a low-going HPD pulse, it must be regarded as a HPD event. Thus, the source must read the link / sink status field or receiver capability field of the DPCD and take corrective action.

4.3 Back-light Specification

Table 4.3 Back-light Characteristics

| Table he back light characteristics | | | | | | | |
|-------------------------------------|--------|-------------------------------|------------|-------|-------|------|-----------------------|
| Item | Symbol | Condit | ions | Min. | Туре. | Max. | Unit |
| Supply Voltage | VF | Only Dooldie | - L | 27 | 29 | 33 | V |
| Supply Current | IF | Only Backlig | 450 | | | mA | |
| Average Brightness | IV | Backlight Current IF=450mA | | 13500 | 14500 | - | Cd/ m ² |
| CIE Color Coordinate | Х | Backlight | Current | 0.29 | 0.32 | 0.35 | _ |
| (Without LCD) | Y | IF=450mA | | 0.33 | 0.36 | 0.39 | |
| Uniformity | В | Backlight IF=450mA | Current | | 70% | _ | % |
| Color | White | | | | | | |

Note: 10 LEDs in series 10 parallel connection.

LCM Brightness: 1200cd/m² (Type.)





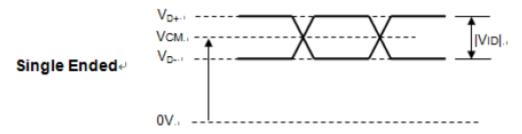
5. AC Characteristics

5.1 Display Port Interface

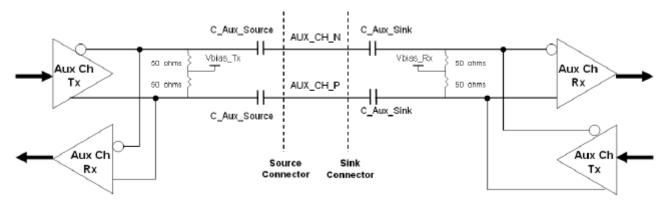
| Parameter | Symbol | Min. | Тур. | Max. | Unit | Notes |
|---|--------------|------|------|------|------|--------|
| Differential Signal Common Mode Voltage(MainLink and AUX) | VCM | 0 | | 2 | ٧ | (1)(4) |
| AUX AC Coupling Capacitor | C_AUX_Source | 75 | | 200 | nF | (2) |
| Main Link AC Coupling Capacitor | C_ML_source | 75 | | 200 | nF | (3) |

Note (1) Display port interface related AC coupled signals should follow VESA DisplayPort Standard Version1.

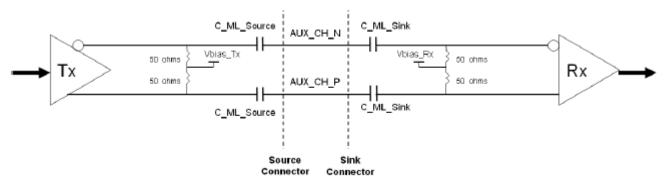
Revision 1a and VESA Embedded DisplayPort™ Standard Version 1.2. There are many optional items described in eDP1.2. If some optional item is requested, please contact us.



(2) Recommended eDP AUX Channel topology is as below and the AUX AC Coupling Capacitor (C_Aux_Source) should be placed on the source device..



(3) Recommended Main Link Channel topology is as below and the Main Link AC Coupling Capacitor (C_ML_Source) should be placed on the source device.

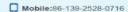


(4) The source device should pass the test criteria described in DisplayPort Compliance Test Specification(CTS)

1.1



Tel: 86-755-23037763







5.2 Color Data Input Assignment

| Color Red Green R7 R8 R5 R4 R3 R2 R1 R0 G7 G8 G5 G4 G3 (Black 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 Red 1 1 1 1 1 1 1 1 1 1 0 0 0 0 0 0 Green 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 Basic Blue 0 0 0 0 0 0 0 0 0 0 1 1 1 1 1 1 Colors Cyan 0 0 0 0 0 0 0 0 0 0 1 1 1 1 1 1 | G2 G1 0 0 0 0 1 1 0 0 1 1 0 0 | G0 0 0 1 0 | B7 0 0 1 | B6 0 0 0 1 | B5 0 0 0 1 | B4 0 0 0 | B3 0 0 | B2 0 0 0 | B1 0 0 | B0 0 0 |
|---|---|------------------------|-------------------|------------------------|------------------------|-------------------|--------------|-------------------|--------------|-----------------------------------|
| Black 0 <td>0 0 0 0 1 1 0 0 1 1</td> <td>0 0 1 0</td> <td>0 0 0 1</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> | 0 0 0 0 1 1 0 0 1 1 | 0 0 1 0 | 0 0 0 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Red | 0 0 1 1 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 | 0 1 0 1 | 0 0 1 1 | 0 | 0 | Ō | 0 | Ŏ | ō | 1 - 1 |
| Green 0 0 0 0 0 0 0 0 1 1 1 1 1 1 Basic Blue 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 | 1 1 0 0 1 1 | 1 0 1 | 0 1 1 | ı | 0 | 1 | 1 | 1 - | ı – | 0 |
| Basic Blue 0 0 0 0 0 0 0 0 0 | 0 0 1 | 1 | 1 | 1 | I - | 0 | 0 | 0 | 10 | |
| | 1 1 | 1 | 1 | 1 | 1 | l 1 | | | I - | 0 |
| Colors Cyan | | 0 | 1 | l 1 | | 1 ' | 1 | 1 | 1 | 1 |
| | 0 0 1 | 0 | 4 | l ' | 1 | 1 | 1 | 1 | 1 | 1 |
| | 1 1 | | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| Yellow 1 1 1 1 1 1 1 1 1 | | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| White 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 | 1 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| Red(0)/Dark 0 0 0 0 0 0 0 0 0 0 0 0 0 | 0 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Red(1) 0 0 0 0 0 0 1 0 0 0 | 0 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Gray Red(2) | 0 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Scale | : : | : | : | : | : | : | : | : | : | : |
| Of : : : : : : : : : : : : | : : | : | : | : | : | 1: | : | : | : | : |
| Red Red(253) 1 1 1 1 1 0 1 0 0 0 | 0 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Red(254) 1 1 1 1 1 1 0 0 0 0 | 0 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Red(255) 1 1 1 1 1 1 1 0 0 0 | 0 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Green(0)/Dark 0 0 0 0 0 0 0 0 0 0 0 0 | 0 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | O |
| Green(1) 0 0 0 0 0 0 0 0 0 | 0 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Gray Green(2) | 0 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | lol |
| Scale : : : : : : : : : : : : : : : : : : | : : | : | : | : | : | : | : | : | : | : |
| Of : :::::::::::::::::::::::::::::::::: | : : | : | : | : | : | 1: | : | : | : | : |
| Green Green(253) 0 0 0 0 0 0 0 1 1 1 | 1 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | lol |
| | 1 1 | 0 | 0 | 0 | Ιo | Ιo | Ιo | Ιo | lo | lol |
| Green(255) 0 0 0 0 0 0 0 1 1 1 | 1 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | lol |
| Blue(0)/Dark 0 0 0 0 0 0 0 0 0 0 0 | 0 0 | 0 | 0 | 0 | 0 | 10 | 0 | 0 | 0 | u |
| Blue(1) 0 0 0 0 0 0 0 0 0 0 0 0 0 0 | olol | 0 | 0 | 0 | lo | 10 | Ιo | lo | Ιo | 1 1 |
| Gray Blue(2) | ōlōl | ōl | ō | Ιō | Ιō | Ιō | Ιō | Ιō | ī | ΙòΙ |
| Scale : : : : : : : : : | : : | - | : | : | : | - | - | - | | |
| Of : : : : : : : : : : : : : : : : : : : | : : | : | | | : | | | : | : | |
| Blue Blue(253) 0 0 0 0 0 0 0 0 0 | o lo l | o l | 1 | 1 | 1 | 1 | 1 | 1 | lò | $\begin{bmatrix} 1 \end{bmatrix}$ |
| Blue(254) | ŏlŏl | ŏl | 1 | 1 | Ιi | i | i | i | 1 | ΙċΙ |
| Blue(255) 0 0 0 0 0 0 0 0 0 | ŏ l ŏ l | ŏl | 1 | 1 | Ιi | l i | 1 | 1 | 1 | 1 |

Note (1) 0: Low Level Voltage, 1: High Level Voltage

5.3 Display Timing Specifications

The input signal timing specifications are shown as the following table and timing diagram.

Refresh rate 60Hz

| Signal | Item | Symbol | Min. | Тур. | Max. | Unit | Note |
|--------|-----------------------------------|--------|--------|--------|--------|------|------|
| DCLK | Frequency | 1/Tc | 151.6 | 152.84 | 154.04 | MHz | - |
| | Vertical Total Time | T∨ | 1128 | 1132 | 1136 | TH | - |
| | Vertical Active Display Period | TVD | 1080 | 1080 | 1080 | TH | - |
| | Vertical Active Blanking Period | T∨B | TV-TVD | 52 | TV-TVD | TH | - |
| DE | Horizontal Total Time | TH | 2240 | 2250 | 2260 | Tc | - |
| | Horizontal Active Display Period | THD | 1920 | 1920 | 1920 | Tc | - |
| | Horizontal Active Blanking Period | THB | TH-THD | 330 | TH-THD | Tc | - |









Refresh rate 50Hz (Power Saving Mode)

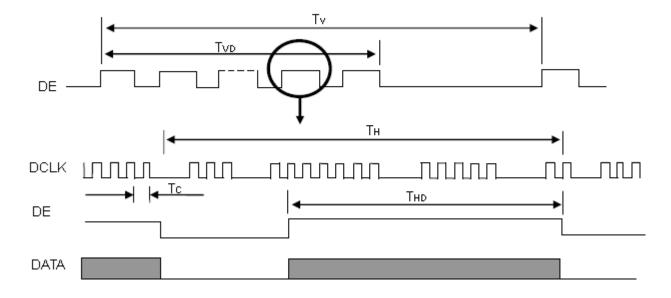
| Signal | Item | Symbol | Min. | Тур. | Max. | Unit | Note |
|--------|-----------------------------------|--------|--------|--------|--------|------|------|
| DCLK | Frequency | 1/Tc | 126.35 | 127.35 | 128.35 | MHz | - |
| | Vertical Total Time | T∨ | 1128 | 1132 | 1136 | TH | - |
| | Vertical Active Display Period | TVD | 1080 | 1080 | 1080 | TH | - |
| | Vertical Active Blanking Period | T∀B | TV-TVD | 52 | TV-TVD | TH | - |
| DE | Horizontal Total Time | TH | 2240 | 2250 | 2260 | Tc | - |
| | Horizontal Active Display Period | THD | 1920 | 1920 | 1920 | Tc | - |
| | Horizontal Active Blanking Period | THB | TH-THD | 330 | TH-THD | Tc | - |

Refresh rate 50Hz (Power Saving Mode)

| Signal | Item | Symbol | Min. | Тур. | Max. | Unit | Note |
|--------|-----------------------------------|--------|--------|--------|--------|------|------|
| DCLK | Frequency | 1/Tc | 121.3 | 122.26 | 123.22 | MHz | - |
| | Vertical Total Time | T∨ | 1128 | 1132 | 1136 | TH | - |
| | Vertical Active Display Period | TVD | 1080 | 1080 | 1080 | TH | - |
| | Vertical Active Blanking Period | T∨B | TV-TVD | 52 | TV-TVD | TH | - |
| DE | Horizontal Total Time | TH | 2240 | 2250 | 2260 | Tc | - |
| | Horizontal Active Display Period | THD | 1920 | 1920 | 1920 | Tc | - |
| | Horizontal Active Blanking Period | THB | TH-THD | 330 | TH-THD | Tc | - |

Note (1) The panel can operate at 60Hz normal mode and power saving mode, respectively. All reliability tests are based on specific timing of 60Hz refresh rate. We can only assure the panel's electrical function at power saving mode.

INPUT SIGNAL TIMING DIAGRAM





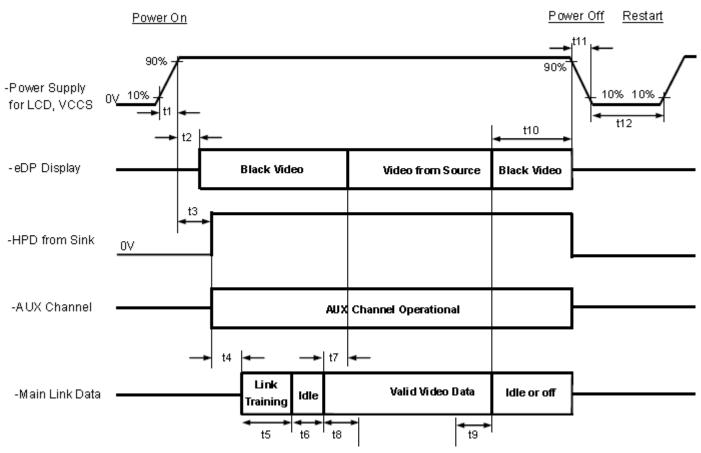








6. Power On/Off Sequence



Timing Specifications:

| Parameter | Description | Reqd. | Va | lue | Unit | Notes |
|-------------|--|--------|-----|-----|------|--|
| Farailletei | Description | Ву | Min | Max | OTIL | NOTES |
| t1 | Power rail rise time, 10% to 90% | Source | 0.5 | 10 | ms | - |
| t2 | Delay from LCD,VCCS to black video generation | Sink | 0 | 200 | ms | Automatic Black Video generation prevents display noise until valid video data is received from the Source (see Notes: 2 and 3 below) |
| t3 | Delay from LCD,VCCS to HPD high | Sink | 0 | 200 | ms | Sink A UX Channel must be operational upon HPD high (see Note:4 below) |
| t4 | Delay from HPD high to link training initialization | Source | 0 | 500 | ms | Allows for Source to read Link capability and initialize |
| t5 | Link training duration | Source | 0 | 500 | ms | Dependant on Source link training protocol |







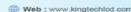




| t6 | Link idle | Source | 0 | 500 | ms | Min accounts for required BS-Idle pattern. Max allows for Source frame synchronization |
|-----|--|--------|----------|-----|----|---|
| t7 | Delay from valid video data from Source to video on display | Sink | 0 | 50 | ms | Max value allows for Sink to validate video data and timing. At the end of T7, Sink will indicate the detection of valid video data by setting the SINK_STATUS bit to logic 1 (DPCD 00205h, bit 0), and Sink will no longer generate automatic Black Video |
| t8 | Delay from valid video data from Source to backlight on | Source | 80 | 500 | ms | Source must assure display video is stable *: Recommended by INX. To avoid garbage image. |
| t9 | Delay from backlight off to end of valid video data | Source | 50 | 500 | ms | Source must assure backlight is no longer illuminated. At the end of T9, Sink will indicate the detection of no valid video data by setting the SINK_STATUS bit to logic 0 (DPCD 00205h, bit 0), and Sink will automatically display Black Video. (See Notes: 2 and 3 below) *: Recommended by INX. To avoid garbage image. |
| t10 | Delay from end of valid video data from Source to power off | Source | 0 | 500 | ms | Black video will be displayed after receiving idle or off signals from Source |
| t11 | VCCS power rail fall time, 90% to 10% | Source | 0.5 | 10 | ms | See Note 5 below |
| | | · | . | | | |
| t12 | VCCS Power off time | Source | 500 | - | ms | - |

- Note (1) Please don't plug or unplug the interface cable when system is turned on.
- Note (2) The Sink must include the ability to automatically generate Black Video autonomously. The Sink must automatically enable Black Video under the following conditions:
 - Upon LCD VCCS power-on (within T2 max)
 - When the "NoVideoStream_Flag" (VB-ID Bit 3) is received from the Source (at the end of T9)
- Note (3) The Sink may implement the ability to disable the automatic Black Video function, as described in Note (2), above, for system development and debugging purposes.
- Note (4) The Sink must support AUX Channel polling by the Source immediately following LCD VCCS power-on without causing damage to the Sink device (the Source can re-try if the Sink is not ready). The Sink must be able to response to an AUX Channel transaction with the time specified within T3 max.
- . Note (5) The VCCS power rail is recommended to rise and fall linearly. If not, please contact us to conduct risk assessment





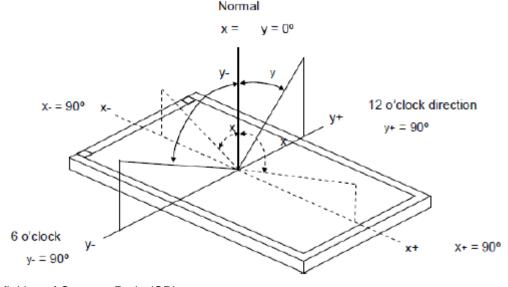


7. Optical Specifications

The relative measurement methods of optical characteristics are shown

| Item | | Symbol | Condition | Min. | Тур. | Мах. | Unit | Note |
|----------------------------|---------------|--------|--|-------|-------|------|------|------------------------|
| | Red | Rx | | | 0.658 | | | |
| | Reu | Ry | | | 0.328 | | | |
| Color | Green | Gx | | | 0.275 | | | |
| Chromaticity (CIE 1931) | Orcan | Gy | | Тур – | 0.574 | Тур+ | | C Light |
| FOG Only | Blue | Вх | | 0.03 | 0.143 | 0.03 | _ | Source |
| with C-light | Dide | Ву | θ _x =0°, θ _Y =0° | | 0.092 | | | (1),(5) (6),(7),(8) |
| | White | Wx | R=G=B=255 Gray scale | | 0.314 |] | | (0)((1)(0) |
| | wille | Wy | Cray scale | | 0.361 | | | |
| Color ga | mut | C.G | | 64 | 69 | - | % | |
| Center Tran | smittance | Т% | | 3.83 | 4.39 | - | % | INX BLU (1),(4),(6) |
| Contrast (| Ratio | CR | | 800 | 1000 | - | - | (2) |
| Respon | se Time | TR+TF | θ _x =0°, θ _Y =0° | - | 25 | 30 | ms | (3),(6) |
| | Horizontal | χ+ | | 80 | - | - | | |
| Viewing Angle | 1701120111.01 | X - | CR>10 | 80 | - | - | Deg. | (1),(5),(6) |
| Vicwing Angle | Vertical | y + | OK210 | 80 | - | - | Dog. | |
| | 70,11001 | у - | | 80 | - | - | | |

Note (1) Definition of Viewing Angle (_x, _y):



Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

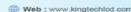
Contrast Ratio (CR) = L255 / L0

L255: Luminance of gray level 255

L 0: Luminance of gray level 0

CR = CR (5)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (6).



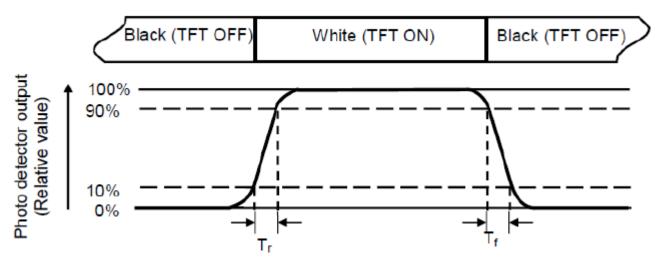


Note (3) Definition of Response Time:

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (Tr) is the time between photo detector output intensity changed from 10% to 90%. And fall time (Tf) is the time between photo detector output intensity changed from 90% to 10%.

$$RT = RT (5)$$

RT (X) is corresponding to the Response Time of the point X at Figure in Note (6).



Note (4) Definition of Luminance of White (Lc):

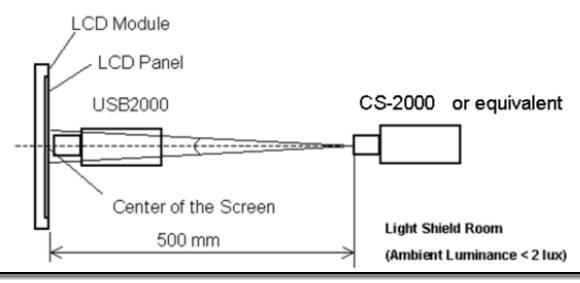
Measure the luminance of gray level 255 at center point

$$LC = L(5)$$

L (x) is corresponding to the luminance of the point X at Figure in Note (6).

Note (5) Measurement Setup:

The LCD module should be stabilized at given temperature for 40 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 40 minutes in a windless room.







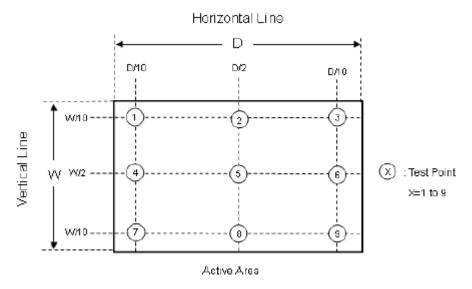






Note (6) Definition of White Variation (_W):

Measure the luminance of gray level 255 at 9 points



Note (7) The listed optical specifications refer to the initial value of manufacture, but the condition of the specifications after long-term operation will not be warranted.

Note (8) Definition of color gamut (C.G%):

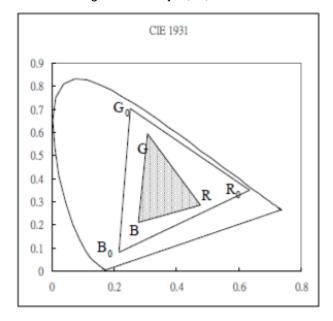
C.G%= R G B / R₀ G₀ B₀,*100%

Ro, Go, Bo: color coordinates of red, green, and blue defined by NTSC, respectively.

R, G, B: color coordinates of module on 255 gray levels of red, green, and blue, respectively.

 $R_0\,G_0\,B_0$: area of triangle defined by $R_0,\,G_0,\,B_0$

R G B: area of triangle defined by R, G, B













8. Reliability Test Items

| No. | Test Item | Test Condition | Check Time |
|-----|---------------------------|----------------------|------------|
| 1 | High temp storage | T=80°C | 96Hrs |
| 2 | Low temp storage | T=-30℃ | 96Hrs |
| 3 | High temp operation | T=7 0℃ | 96Hrs |
| 4 | Low temp operation | T=-20°C | 96Hrs |
| 5 | High temp & high humidity | T=5 0°C H=90% | 96Hrs |

Note 1: The test samples have recovery time for 2 hours at room temperature before the function check. In the standard conditions, there is no display function NG issue occurred.

Note 2: After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

Note 3: Under no condensation of dew.









9. Handling Precautions

9.1 Safety

The liquid crystal in the LCD is poisonous. Keep away from your mouth and eyes. If the liquid crystal contacts with your skin, mouse or clothes, use soap to wash it off immediately.

9.2 Handling

- i. The LCD panel is made of very thin glass. Mechanical impact or extrusion to the surface should be prevented.
- ii. The polarizer attached on the display is very easy to be damaged, handle it with special attention.
- iii. To avoid contamination on the display surface, do not touch the display surface with bare hands.
- iv. The transparent electrodes may be disconnected if you use the LCD panel under dew-condensing environment.
- v. The characteristics of the semiconductor devices may be affected when they are exposed to light, possibly resulting in malfunctioning of the ICs. To prevent such malfunctioning of the ICs, make sure the application and the mounting of the panel are designed so that the IC is not exposed to light.

9.3 Static Electricity

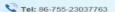
Ground soldering iron tips, tools and testers when you operate. Also ground your body when handling the products and store the products in an anti-electrostatic container.

9.4 Storage

Store the products in a dark place where the temperature is within the range of 25±10℃ and with low humidity (60%RH or less). Do not store the LCD product in an atmosphere containing organic solvents or corrosive gases.

9.5 Cleaning

Do not wipe the polarizer with dry cloth, as it might cause scratching. Wipe the polarizer with a soft cloth soaked with petroleum IPA. Other chemical might damage the panel.









10. QC

10.1目的

制定 15.6 寸模组产品出货检验标准,明确检验内容和规范;

10.2 范围

适用康泰科技有限公司,使用原装模组、FOG或自购LCD所生产的模组、FOG出货;

10.3 定义

黑白点:显示的点是黑色或者白色,当对比变化时,这些缺陷在大小或强度上没有变化;

亮点:显示中的点(子像素),在显示区域显示为明亮,大小大于 1/2dot,通过 5%的 ND 卡在黑色画面下可见:

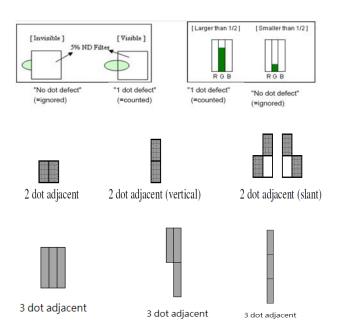
碎亮点: 在黑画面下看到的发亮的, 大小小于 1/2dot 的点;

暗点:显示中的点(子像素),大小大于 1/2dot,在红绿蓝画面的显示区域显示为黑色;

Mura: 相对于显示区域的部分背景亮度,部分区域更暗或者更亮的现象(显示画面不均匀);

二连点:两个相邻的 dot 点;

三连点: 三个相邻的 dot 点;



10.4 检验条件

- 10.4.1 环境要求: 温度: 25±5℃、湿度: 30%-70%RH;
- 10.4.2 外观检验灯光: 20w 荧光灯或等效照度;
- 10.4.3 检验距离: 30cm;

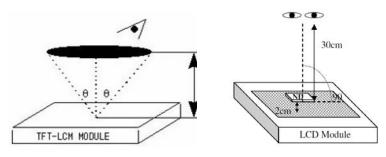




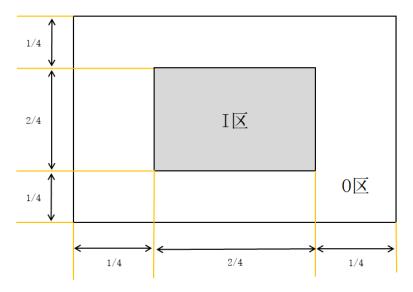




- 10.4.4 电性检验照度要求: 100-200Lux;
- 10.4.5 外观检验照度要求: 500-700Lux;
- 10.4.6 ND 卡检验要求: ND 放于距 cell 表面 2cm 处, 从 30cm 距离检验;
- 10.4.7 检验角度: 做垂直方向±45°, 水平方向±45°夹角检验;



10.4.8 显示区域划分:将显示区长度和宽度方向均匀分为4等分,中间2/4区域为I区,四周为0区,见 下图:

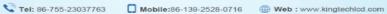




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10.5 电性检测

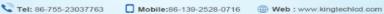
| | | | | | | | | | 检验 | 标准 | | | | | | | | | | | | | |
|--------------|-----------------|---|----------------------|-----------|----------------------------------|------------------|-------------------|---|--------------|------------|--|----------------|------------|------|---------|------------|------|----|------|--|--|----|--|
| 检验项目 | | AO 规 | | | A1 规 | | | A2 规 | | | A3 规 | | | A4 规 | | | B规 | | | | | | |
| | ΙZ | 0区 | 总数 | ΙZ | 0区 | 总数 | ΙZ | 0区 | 总数 | Ⅰ区 0区 总数 | | ΙZ | 0区 | 总数 | ΙZ | 0区 | 总数 | | | | | | |
| 表点 | | N≤O | | | N≤2 | | 2014 | 54 | N≤5. | | N≤S | | N≤10 | | N≤10 不计 | | 不计 | | | | | | |
| 二進奏点 | | N≤O | | | N≤O | | 814 | 5 1 | os≽ | | N≤S | | | N≤10 | | | 不计 | | | | | | |
| 三巡奏点 | | N≤O | | | N≤O | | Na | ≦0 | 8 m m | | N≤S | | | N≤10 | | | 不计 | | | | | | |
| 暗点 | N≤1 | N≤5, DS ≥ 10mm | N≤5, DS ≥ 10mm | N≤5, D: | 5≥10mm | N≤8, DS ≥ | Na | ≦7 | N≤7, DS ≥ | | N≤10 | | N≤10 | | N≤10 | | N≤10 | | N≤20 | | | 不计 | |
| 二進時点 | | N≤O | | 1914 | ši | 10mm | N≤2, DS | ≥ēm | Smm. | | N≤7 | | | N≤20 | | | 不计 | | | | | | |
| 三進時点 | | N≤O | | N# | ≦0 | | 1014 | 5 1 | 1 | | N≤7 | | | N≤20 | | | 不计 | | | | | | |
| 美点+暗点 | N≤ | .5. DS≥1 | Omn | N= | ≦6, DS ≥ 10 | en. | N= | ≦7. DS≥8 | - | | / / | | / | | | | | | | | | | |
| 碎壳点 | 10%ND j | 2 挡不可见 | 2. 无视 | 5900D jij | 档不可见 | - 无视 | 不计 ⑷ | 崇天星不可 | 7) | 不计 (擴天星不可) | | 下町) | 不计 (擴天星不可) | | 不计 | | | | | | | | |
| 点状异物点、 | 0.15mm ← | im, 数是 <d≤0.3m i0m; :D≤0.4m</d≤0.3m | n, N≤5, | | m,数量) :D ≤ 0. Sem :Omn; | | 0.5mm< (6]8E≥: | m,数量) :D≤0.8mm :Omn; :D≤1.2mm | . N≤5. | | 不计 | | | 不针 | | | 不计 | | | | | | |
| 线状异物 | il : | m, LSO. m, LSOn m; | | 0. imm() | m, 不计; r=30.2mm, .es4mm, N | | 0. innet | m, 不计; r = 0.5mm, . = 5mm, N | | O. innex | ws0.inn, 不计; 0.inn(ws0.8mm, 0.8mm(1.54mm, N≤5; | | 不 计 | | | | 不计 | | | | | | |
| POL气泡 | 0.2mm< DS≥: | m. 不计; :D≤0. Bm :Omn 线状异物学 | . N≤2. | 标准; | 点状不良都 也不进入! | 於競异物 5M 区 1/2 | 标准; | 以状不良。 也不进入。 | | | 不计 8 | | 不计 | | 5%) | 5%00 返指不可见 | | 不守 | | | | | |
| Mura | 50mm=80 | . 角判定 mm.不可见 muxa.按 10 为 0x | 为 ox | | R外页面不 见,不计 | 可见。黑 | 5%X | □透挡不可 | 可見 | 6900 巡挡不可见 | | 透挡不可见 不影响电性 OK | | OK | | 不计 | | | | | | | |
| 功能統陷 | | | | | | 묘줐 | 异常、敏 | 战(模战、 | 竖线等)。 | 、无显、每 | 學影响功能 | 能的不良不 | 可省 | | | • | | | | | | | |



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10.6 外观检验

| | | 检验标准 | | | | | | | | |
|-----------------|------|---|---|--|----------------------------|------------|-----|--|--|--|
| 检验项目 | 图示 | AO 规 | A1 规 | A2 规 | A5 规 | A4 规 | 5 规 | | | |
| PAD 区角落破损 | × | I≤1.8m. T≤1. | .8mm. Z≤T. N≤2 不(| 为及线路,显示正常 | 不伤及线路,显示正常 | | | | | |
| PAD 区边缘破损 | Z Z | T≤0.5, I≤2. | 0. Z≤T. N ≤2 不伤 | 及线路。显示正常 | 不伤及线路,显示正常 | | | | | |
| 非 PAD 区角落破 機 | · · | I≤1.8mm, T≤1. | 8mm. Z≤T. N≤2 . 주 | 6. 6. 5. 5. 5. 5. 5. 5. 5. 5. 5. 5. 5. 5. 5. | : | 不伤及战路,显示正常 | ; | | | |
| 非 PAD 区边缘破损 | Z Z | I≤1.8m. Y≤0. | 3mm. z≤ī, N 不计,7 | P伤及线路。显示正常 | ; | 不伤及线路,显示正常 | | | | |
| 表面点 | 无函 | | 0≤0.5mm,数量 N 不计 D≤0.8mm,N≤5,同题 | | 不针 | | | | | |
| 表面划伤 | 无函 | 0. innew | ₩ ≤ 0.1mm,不计; ≤ 0.2mm, 0.8mm < 1.≤ 4mm | ı. N≤5; | 不计 | | | | | |
| FPC/FCS | 无函 | 1. 定位孔破损不可有 2. FPC 死折不可有 3. 元件股落不可有 4. 元件成為、这结合 6. 金手指断、脱不可 6. 元件卡不到位不可 7. FPC 组裁对位信移 8. FPC 上双面放脱弱 9. 据鉴不能检动,7 | 等海袋不良不可有 可有 可有 5不可有 5或异常不可有 | | 1. 元件股落,不可相 2. 元件最终、这级6 | | | | | |
| 保护膜 | 无函 | | 1. 拟破不可有,其他划伤、时记等无视 2. 品抓贴按图纸贴附 | | | | | | | |
| 脱纹 | Cnek | 不可有 | | | | | | | | |

1. W: Width

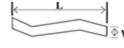
2. L: Length

3. D: Average Diameter

4. N: Count







W: width . L: length

10.7 质保期限

10.7.1 保修 12 个月(如有合同,以合同为准)。

end.

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