

TEL: 86-755- 23037763

Mobile: +86-139-2528-0716

Web: www.kingtechdisplay.com



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TFT LCD MODULE 2.4 inch 240RGB*320DOTS

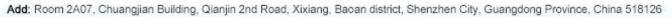
MODULE NUMBER: PV0241F2CZ0

REVISION: V00

Cus	tomer Approval:
	Approved For Specifications
	Approved For Specifications & Sample

Prepared by	Checked by	Approved by
李东洋		

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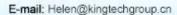


Document Revision History

Version	Date	Page	Description	Changed By
V00	2015-10-28	-	First issue	李东洋

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E-mail: Helen@kingtechgroup.cn

TEL: 86-755-23037763

Mobile: +86-139-2528-0716

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1. LCM Specification

1.1 Description

PV0241F2CZ0 is a transmissive type color active matrix liquid crystal display(LCD) which uses amorphous thin film transistor(TFT) as switching devices. This product is composed of a TFT LCD panel, a drive IC, a FPC and a LED-backlight unit. The active display area is 2.4 inches diagonally measured and the native resolution is 240*RGB*320.Features of this product are listed in the following table.

1.2 Functions & Features

Table 1.1 Module Functions & Features

Parameter	Value	Unit					
LCD Mode	TFT/Transmissive	-					
Color Depth	262 K	-					
Display Resolution	240RGB*320	pixels					
Module Size	60.4(H)*42.72(W)*2.21(T)(Exclude FPC)	mm					
Active Area (A.A)	48.96(H)*36.72(W)	mm					
Pixel Arrangement	RGB-stripe	-					
Viewing Direction	ALL O' clock						
Display Mode	Normally Black						
LCD Controller/Driver	ILI9341V	-					
IC Package Type	COG	-					
Interface	16-bits MPU	-					
Power Supply Voltage	2.5~3.3	V					
LCM Brightness	250	cd /m²					
Back-light	White LED*4	PCS					

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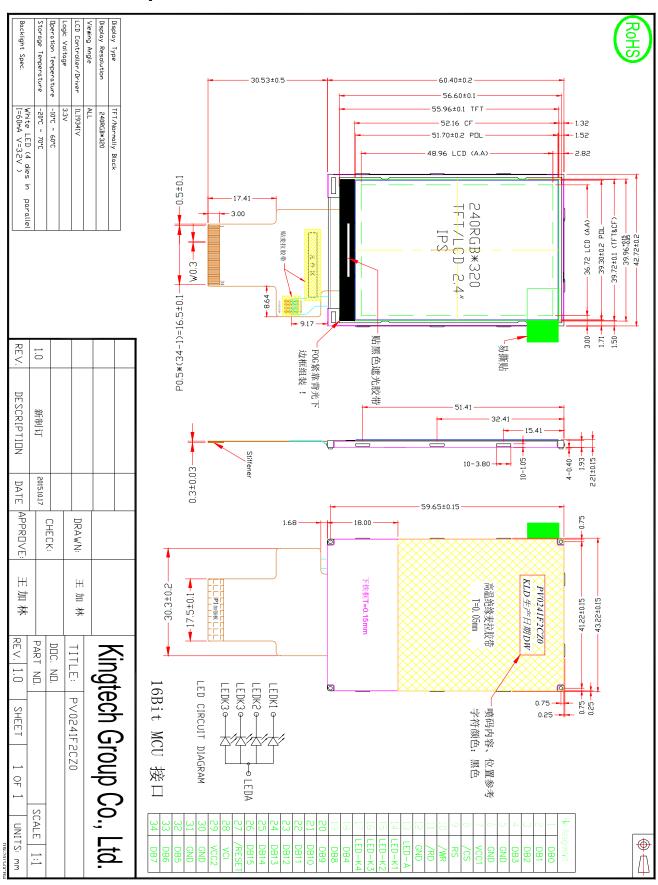


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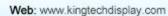
2. Mechanical Specification





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3. Pin Descriptions

Pin No.	Symbol	1/0	Functional	Remark
1~4	DB0~DB3	I/O	Data bus.	
5~6	GND	P	System ground.	
7	VCC1	P	Power supply, 2.5~3.3V.	
8	CS	I	80-system: Chip select pin.	
9	RS	I	Register select signal.	
10	WR	I	Serves Write signal.	
11	RD	I	Read strobe signal.	
12	GND	P	System ground.	
13	LEDA	P	LED Power supply +.	
14~17	LEDK1~ LEDK4	P	LED Power supply	
18	DB4	I/O	Data bus.	
19~26	DB8~DB15	I/O	Data bus.	
27	RESET	I	Reset signal pin.	
28	VCI	P	Power supply, 2.5~3.3V.	
29	VCC2	P	Logic Power supply: 1.65~3.3V.	
30~31	GND	P	System ground.	
32~34	DB5~DB7	I/O	Data bus.	

说明: 本屏为 16-bits 的 MPU 接口

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4. Electrical Units

4.1 Absolute Maximum Ratings

The absolute maximum ratings are list on Table 4.1. When used out of the absolute maximum ratings, the LCM may be permanently damaged. Using the LCM within the following electrical characteristics limit is strongly recommended for normal operation. If these electrical characteristic conditions are exceeded during normal operation, the LCM will malfunction and cause poor reliability.

Table 4.1 Module Absolute Maximum Ratings

Item	Symbol	Unit	Value	Note
Power Supply Voltage (1)	Vdd	V	-0.3 to +4.5	
Power Supply Voltage (2)	VGH ~ VSS	V	-0.3 to +15.0	
Power Supply Voltage (3)	VSS ~ VGL	V	0 to -7.5	
Operating Temperature	Тор	°C	-10 to +60	
Storage Temperature	Tst	°C	-20 to +70	
Operating Humidity	Нор	%(RH)	10~90	

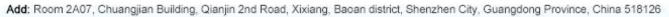
(VSS=0V)

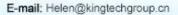
4.2 Electrical characteristics (Ta=25°C)

Table 4.2:DC Characteristic (Vcc = 3.0 ~ 3.6V)

Item		Symbol	Condition	Min.	Type.	Max.	Unit
Supply	Power	VCI		1.65	2.8	3.3	V
Voltage	Logic	IOVCC		2.5	2.8	3.3	V
Input	H level	ViH		0.7Vdd		Vdd	V
Voltage	L level	VIL		0		0.3V _d	
Current Consumption		loo	With internal voltage generation; VDD=2.8V Tamb=2 5°C;		10		mA

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4.3 Back-light Specification

Table 4.3 Back-light Characteristics

Item	Symbol	Conditions	Min.	Type.	Max.	Unit
Supply Voltage	VF	Only Backlight	2.9	3.2	3.4	V
Supply Current	IF	Only backlight	60			mA
Average Brightness	IV	Backlight Current IF=60mA	6000	6500	-	Cd/m ²
CIE Color Coordinate	Х	Backlight Current	0.25	0.28	0.30	
(Without LCD)	Y	IF=60mA	0.26	0.28	0.31	_
Uniformity	В	Backlight Current IF=60mA	60%			%
Color		V	/hite			

Note: 4 LEDs in parallel connection.

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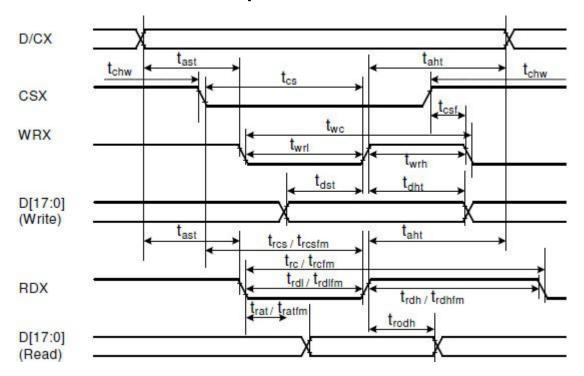
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5. AC Characteristics

5.1 Parallel MPU interface operation



Signal	Symbol	Parameter	min	max	Unit
DOV	tast	Address setup time	0	2	ns
DCX	taht	Address hold time (Write/Read)	0	-	ns
	tchw	CSX "H" pulse width	0	12	ns
	tcs	Chip Select setup time (Write)	15		ns
CSX	trcs	Chip Select setup time (Read ID)	45	<u> </u>	ns
	trcsfm	Chip Select setup time (Read FM)	355	155	ns
	tcsf	Chip Select Wait time (Write/Read)	10	j se	ns
WRX	twc	Write cycle	66		ns
	twrh	Write Control pulse H duration	15	19	ns
	twrl	Write Control pulse L duration	15	192	ns
	trcfm	Read Cycle (FM)	450	8	ns
RDX (FM)	trdhfm	Read Control H duration (FM)	90	<u> </u>	ns
	trdlfm	Read Control L duration (FM)	355	-	ns
	trc	Read cycle (ID)	160	j e	ns
RDX (ID)	trdh	Read Control pulse H duration	90	-	ns
	trdl	Read Control pulse L duration	45	18	ns
54.7.01	tdst	Write data setup time	10	2	ns
D[17:0],	tdht	Write data hold time	10	100	ns
D[15:0],	trat	Read access time	2	40	ns
D[8:0], D[7:0]	tratfm	Read access time		340	ns
	trod	Read output disable time	20	80	ns

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E-mail: Helen@kingtechgroup.cn TEL; 86-755- 23037763

Mobile: +86-139-2528-0716

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6. Power On/Off Sequence

VDDI and VCI can be applied in any order.

VCI and VDDI can be powered down in any order.

During power off, if LCD is in the Sleep Out mode, VCI and VDDI must be powered down minimum

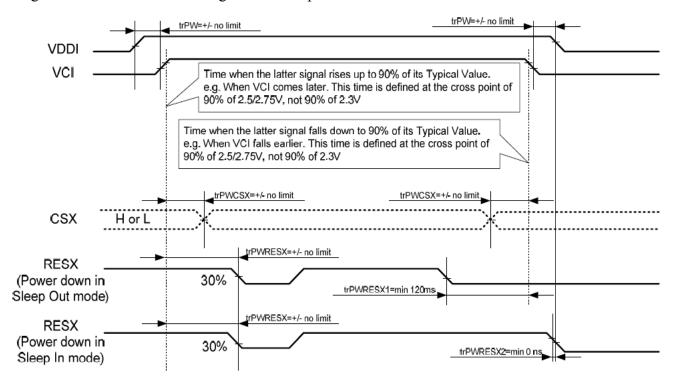
120msec after RESX has been released. During power off, if LCD is in the Sleep In mode, VDDI or VCI can be powered down minimum 0msec after RESX has been released. CSX can be applied at any timing or can be permanently grounded. RESX has priority over CSX.

- Note 1: There will be no damage to the display module if the power sequences are not met.
- Note 2: There will be no abnormal visible effects on the display panel during the Power On/Off Sequences.
- Note 3: There will be no abnormal visible effects on the display between end of Power On Sequence and before receiving Sleep Out command. Also between receiving Sleep In command and Power Off Sequence.

Note 4: If RESX line is not held stable by host during Power On Sequence as defined in Sections 12.1 and 12.2, then it will be necessary to apply a Hardware Reset (RESX) after Host Power On Sequence is complete to ensure correct operation. Otherwise function is not guaranteed.

6.1 Case 1 – RESX line is held High or Unstable by Host at Power ON

If RESX line is held High or unstable by the host during Power On, then a Hardware Reset must be applied after both VCI and VDDI have been applied – otherwise correct functionality is not guaranteed. There is no timing restriction upon this hardware reset.



trPWRESX1 is applied to RESX falling in the Sleep Out Mode trPWRESX2 is applied to RESX falling in the Sleep In Mode

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E-mail: Helen@kingtechgroup.cn

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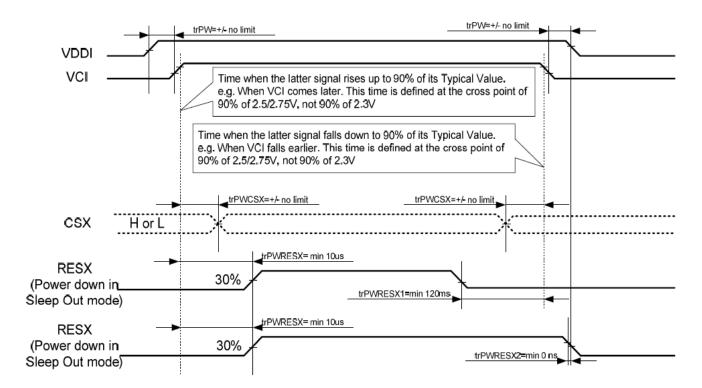
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6.2 Case 2 – RESX line is held Low by Host at Power ON

If RESX line is held Low (and stable) by the host during Power On, then the RESX must be held low for minimum 10µsec after both VCI and VDDI have been applied.



trPWRESX1 is applied to RESX falling in the Sleep Out Mode trPWRESX2 is applied to RESX falling in the Sleep In Mode

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7. Optical Specifications

7.1 Optical Specifications

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note	
Transmittance (with Polarizer)		T (%)		_	4.65	-	%	Measuring with Polarizer , Reference Only	
Transmittance (without Polarizer)		T (%)		_	14.6	-	%		
Contrast		CR		640	800	_	_	(1)(2)	
Response	Rising	T _R		_	16	21		(4)(0)	
time	Falling	T _F	Θ=0	_	19	24	msec	(1)(3)	
Color gamut	(%)		Normal viewing	_	70	_	%	C-light	
	White	W _x	angle	0.290	0.310	0.330	_		
		W_y		0.316	0.336	0.356			
	Red	R _x		0.627	0.647	0.667	_		
Color chromaticity		R _Y		0.297	0.317	0.337	_	(1)(4)	
(CIE1931)	Green	G _x		0.255	0.275	0.295	_	CF glass	
,		G _Y		0.562	0.582	0.602			
	Blue	B _x		0.120	0.140	0.160	_		
	blue	B _Y		0.068	0.088	0.108			
	11	ΘL		_	80	_		(4)(4)	
	Hor.	Θ _R	OD: 40	_	80	_		(1)(4) Measuring with	
Viewing angle	1/	Θυ	CR>10	_	80	_	_	Polarizer ,	
	Ver.	Θ _D		_	80	_		Reference Only	
Optima View D	irection			Free	е			(5)	

7.2 Measuring Condition

■ Measuring surrounding: dark room

■ Ambient temperature: 25±2oC

■ 15min. warm-up time.

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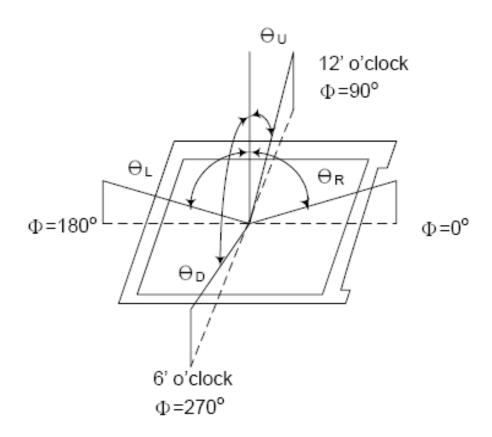
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7.3 Measuring Equipment

■ FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.

Note (1) Definition of Viewing Angle:



Note (2) Definition of Contrast Ratio (CR): measured at the center point of panel

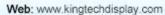
CR = Luminance with all pixels white

Luminance with all pixels black

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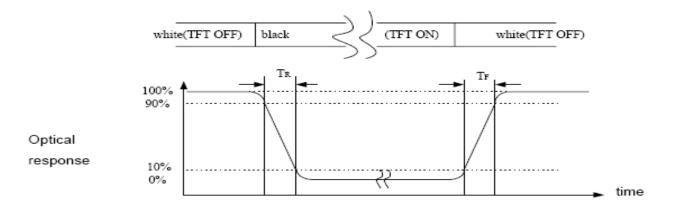


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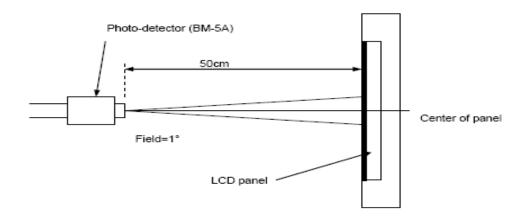




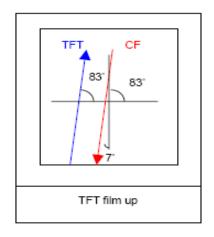
Note (3) Definition of Response Time : Sum of T_R and T_F



Note (4) Definition of optical measurement setup



Note (5) Rubbing Direction (The different Rubbing Direction will cause the different optima view direction.



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8. Reliability Test Items

No.	Test Item	Test Condition	Check Time
1	High temp storage	T=70°C	72Hrs
2	Low temp storage	T=-20℃	72Hrs
3	High temp operation	T=60°C	72Hrs
4	Low temp operation	T=-10℃	72Hrs
5	High temp & high humidity	T=60°C H=90%	72Hrs

Reliability Test Criteria:

Display function should be no change under normal operating condition.

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TEL: 86-755-23037763

Mobile: +86-139-2528-0716

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9. Handling Precautions

9.0 GENERAL PRECAUTION

9.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

9.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

9.3 Breakage of LCD Panel

- 9.3.1.If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 9.3.2. If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 9.3.3. If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 9.3.4. Handle carefully with chips of glass that may cause injury, when the glass is broken.

9.4 Electric Shock

- 9.4.1. Disconnect power supply before handling LCD module.
- 9.4.2. Do not pull or fold the LED cable.
- 9.4.3. Do not touch the parts inside LCD modules and the fluorescent LED's connector or cables in order to prevent electric shock.

9.5 Absolute Maximum Ratings and Power Protection Circuit

- 9.5.1. Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 9.5.2. Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 9.5.3. It's recommended to employ protection circuit for power supply.

9.6 Operation

- 9.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead.
- 9.6.2 Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- 9.6.3 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.
- 9.6.4 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.
- 9.6.5 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

9.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

9.8 Static Electricity

- 9.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- 9.8.2 Because LCD module use CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge. Persons who handle the module should be grounded through adequate methods.

9.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

9.10 Disposal

When disposing LCD module, obey the local environmental regulations.

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TEL: 86-755- 23037763

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10. QC

10.1 Purpose

To ensure the stability of our product and standardize our inspection

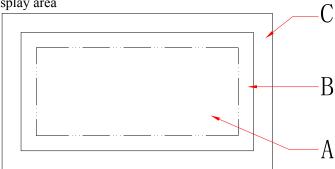
10.2 Application Range

This standard is applied to all 3.5 inch and smaller sized LCM product from Elsun Technology Co.Ltd

10.3 Definition of inspection area

C area: The area covered after installation

B area: visible area A area: display area

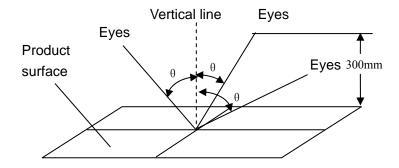


10.4 The environmental condition of inspection

Lighting conditions should be $20 \sim 40 \text{W}$ fluorescent lamp (illumination at $1000 \pm 200 \text{ lux}$)

Test ambient temperature should be 23 ± 5 °C, humidity at 50 ± 20% RH

The tested products should be placed 300mm away from the examiner's eye, and 30 degrees in the vertical direction observed within the region



10.5 Identification

- 10.5.1 Bright dot: dots appearing bright and unchanged in size when the LCD panel is under black pattern.
- 10.5.2 Dark dot: dots appearing dark and unchanged size when the LCD panel is under RGB picture.

10.6 Inspection items and criteria

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10.6.1 Serious defect

1				
	No	inspection item	inspection criteria	defect grade
	10.6.1.1	function failure	 Non-display not allowed Line missing not allowed Invalid touch and drift not allowed (if need) 	main defect
	10.6.1.2	break	broken display not allowed	main defect
	10.6.1.3	dimension	Dimension tolerance out of specified in the drawing not allowed.	main defect

10.6.2 Appearance defect

No	Inspection item	inspection criteria				defect grade							
10.6.2.1	Dot defect black dot, white dot, dirt on surface, stain, bubble		1. dot defect identification: $\Phi = \frac{(\chi + \gamma)}{2}$										
		2. inspection crit	eria range			Minor defect							
			Area and quant		quantity allow	wed							
		ity dimension(mm)	A area	B area	C area								
									Φ ≤0.15	ign	ore		
		0.15< Φ ≤0.2	2 (spacing	>=10mm)	iomoro								
			0.2< Φ ≤0.25	1		ignore							
		$\Phi > 0.25$	()									

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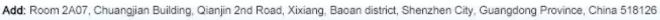
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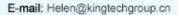


ſ									
	No	Inspection item	inspection criteria			defect grade			
			identification dimension L: length W: width	n of line			3		
			2. inspection or	iteria					
	10.6.2.2	line defect visible	dimensio	on(mm)	quantity allowed (total 3 pcs)		Minor defect		
		black/white line	L (length)	W (width)		area			
			_(::::8:::)	()	A area	B area	C area		
			ignore	W≤0.03	ign	ore			
			L≤3. 0	0.03 <w≤ 0.05</w≤ 	2	2			
				L≤3. 0	0.05 <w≤ 0.08</w≤ 		1	ignore	
				W>0.08		cording to lefect			
			1-If the scratch to 10.6.2.2 2-If the scratch non-working sta	ch is visible	at spec	ial ange	el or at		
		.3 scratch	dimension	n (mm)	Qua	antity allow	wed		
			I (langth)	gth) W (width)	area				
	10.6.2.3		scratch L (length)		A	В	С	Minor defect	
				ignore	W≤0.03	igr	nore		
			5.0 <l≤10. 0<="" td=""><td>0.03<w≤ 0.05</w≤ </td><td>:</td><td>2</td><td>ignore</td><td></td></l≤10.>	0.03 <w≤ 0.05</w≤ 	:	2	ignore		
			L≤5. 0 0.05 <w≤ 0.08<="" td=""><td colspan="2">1</td><td></td></w≤>		1				
				W>0.08	Not a	llowed			

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No	Inspection item	inspection criteria	defect grade
10.6.2.4	Glass defect	1. broken angle X 不计 Y≤2.0mm or X≤2.0mm Y 不计 Meanwhile Z <t 2.="" broken="" ignore="" ignore<="" meanwhile="" other="" part="" td="" x≤5.0mm="" y≤0.8mm="" z≤t=""><td>Minor defec</td></t>	Minor defec
10.6.2.5	Newton ring	1.regular Newton ring 1. Newton ring area>1/3 T/P area; not acceptable. 2. Newton ring area ≤ 1/3 T/P area and doesn't affect the display result and no line distortion; acceptable 2. Non-regular Newton ring 1. Newton ring area > 1/2 T/P area, or no matter how big as long as it affects the display result; not acceptable Newton ring area ≤ 1/2 T/P area, and doesn't affect the display result and witouth line distortion; acceptable	Minor defec

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NO	Inspection item	inspection criteria	defect grade
		 copper foil off, warping, crack and oxidation are not allowed FPC crack, break, serious scratch and crease are not allowed 	main defect
10.6.2.6	FPC	 if no special requirements, no release paper on double-sided adhesive FPC is not allowed. Slight creases and scratches not exposed from the copper foil and with no affect to appearance and function are allowed. if no special requirements, no insulating tape at welding part on backlight and touch-screen is not allowed. Parts off, breakage and deform are not allowed. print on the surface should be clear and correct. 	Minor defect
10.6.2.7	basic appearance requirements	 clean appearance, no dirt, fingerprints and other traces. ITO circuit on COG coating area should not be exposed. Rust, sever scratch, deformation, obvious burrs and color dirt are not allowed. Mis-assembly, part missing are not allowed. Bubble caused by mis-pasted polaroid refers to 10.6.2.1 For watermark, the criteria is upon agreed by both parties. 	Minor defect

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KINGTECH Kingtech Group Co., Ltd. --- Lcd Touchscreen Expert



E-mail: Helen@kingtechgroup.cn

TEL: 86-755-23037763

Mobile: +86-139-2528-0716

Web; www.kingtechdisplay.com



10.6.3 electric defect

No	Inspection item	inspection criteria	defect grade
10.6.3.1	picture defect	Non-display, more or less image and display defect are not allowed.	main defect
10.6.3.2	bright/dark line	Not allowed.	main defect
10.6.3.3	display dot defect	 one dot is acceptable. Under bright status, 2 dark dots with more than 5mm distance is allowed. Totally 2 bright or dark dots are acceptable. The other defect under bright status refers to 10.6.2.1 Note: Electric bright/dark dot means one pixel; less than 1/2 of 1 pixel can be ignored. 	Minor defect
10.6.3.4	connected dot/line defect	 Two continuous defect pixel connected dots are not allowed. Line defect refers to 10.6.2.2 	Minor defect
10.6.3.5	wrong view direction	Wrong view directions, such as opposite view angle, are not allowed.	main defect
10.6.3.6	back light defect	 Backlight off are not allowed. Uneven light, dead light, flicker light, dark angle, light leakage are not allowed. Brightness should comply with drawing 	main defect

end.

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